

Microwave low noise amplifier npn silicon epitaxial transistor

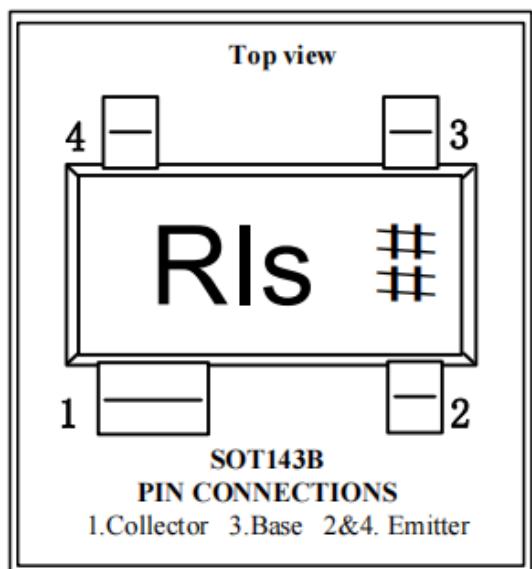
Bfp196 NPN transistor

Description

- This chip is manufactured using silicon epitaxial technology, featuring high power gain, wide bandwidth, and characteristics of low noise, low leakage current, and small junction capacitance. It offers a large dynamic range and ideal current linearity;
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- It is primarily used in ultra-high frequency microwave, VHF, UHF, and CATV high-frequency broadband low-noise amplifiers, such as satellite TV tuners, digital TV set-top boxes, CATV amplifiers, analog-digital cordless phones, radar motion detectors, wireless security alarms, RF modules, and fiber optic modules;

Features

- Collector-emitter breakdown voltage: BVCEO=12V
- maximum collector current: ICM=150mA
- power dissipation: PC=700mW
- characteristic frequency: fT=9.0GHz
- Package type: SOT143B, Body marking: RIs-##.



Maximum Ratings ($T_{amb}=25^{\circ}\text{C}$)

Parameter	Symbol	Value	Unit
collector-base voltage	V_{CBO}	20	V
collector-emitter voltage	V_{CEO}	12	V
emitter-base voltage	V_{EBO}	2.0	V
collector current	I_{CM}	150	mA
power dissipation	P_T	700	mW
chip junction temperature	T_J	150	°C
storage temperature	T_{stg}	-65~+150	°C

Electrical Characteristics (T_{amb}=25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
collector-base breakdown voltage	BV _{CBO}		20			V
collector-emitter breakdown high voltage	BV _{CEO}		12			V
collector-emitter breakdown voltage	BV _{CEO}		2.5			V
collector cut-off current	I _{CBO}	V _{CB} =10V, I _E =0			0.1	µA
DC current amplification factor	h _{FE}	V _{CE} =8V, I _C =50mA V _{CE} =6V, I _C =30mA	75	100 110	140	
characteristic frequency	f _T	V _{CE} =8V, I _C =70mA, f=500MHz	8.5	9.0		GHz
collector-emitter capacitance	C _{ce}	I _C =I _E =0, V _{CE} =10V, f=1MHz		0.35		pF
collector-base capacitance	C _{cb}	I _E =I _C =0, V _{CB} =10V, f=1MHz,		0.8	1.3	pF
emitter-base capacitance	C _{eb}	I _C =I _E =0, V _{EB} =0.5V, f=1MHz		3.9		pF
insertion power gain	S ₂₁ ²	I _C =50mA, V _{CE} =8V, f=900MHz	13	14		dB
		I _C =50mA, V _{CE} =8V, f=1.8GHz	6.5	7.5		dB
noise figure	NF	V _{CE} =8V, I _C =20mA, f=900MHz		1.3		dB
		V _{CE} =8V, I _C =20mA, f=1.8GHz		2.2		dB
maximum unilateral power gain	G _{UM}	I _C =50mA, V _{CE} =8V, f=900MHz	16	17		dB
		I _C =50mA, V _{CE} =8V, f=1.8GHz		10		dB
output power at 1dB gain compression	PL1	I _C =50mA, V _{CE} =8V, R _L =50Ω, f=900MHz		20		dBm
third-order intercept point	ITO	I _C =50mA, V _{CE} =8V, R _L =50Ω, f _p =900MHz, f _q =902MHz		33		dBm

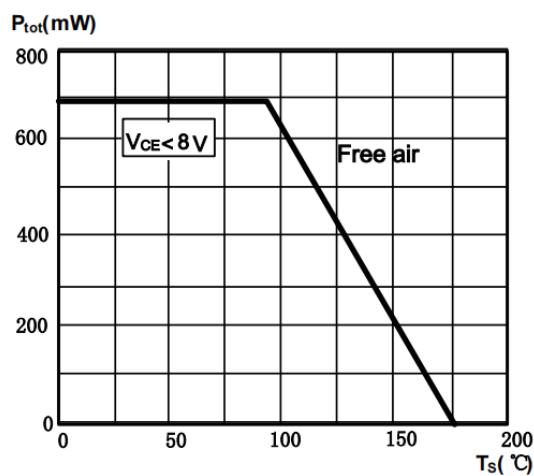
Note:

$$G_{UM} = 10 \log \frac{|S_{21}|^2}{(1 - S_{11})^2 (1 - S_{22})^2} \text{ dB}$$

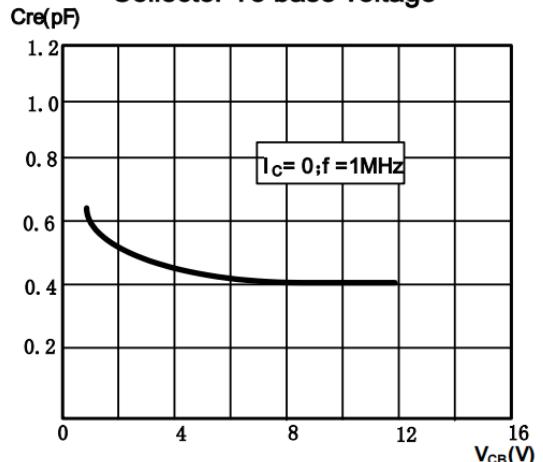
Typical characteristic curves

Typical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise specified)

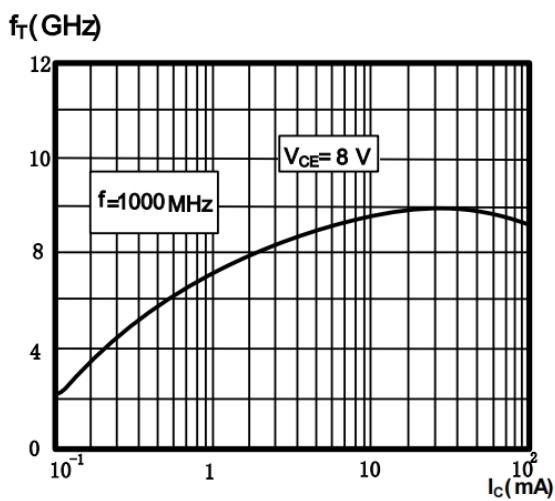
Total Power Dissipation vs. Ambient Temperature



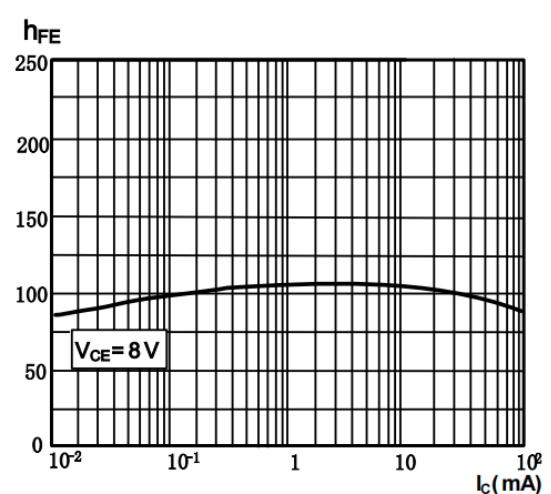
Reverse Transfer Capacitance vs. Collector To base voltage



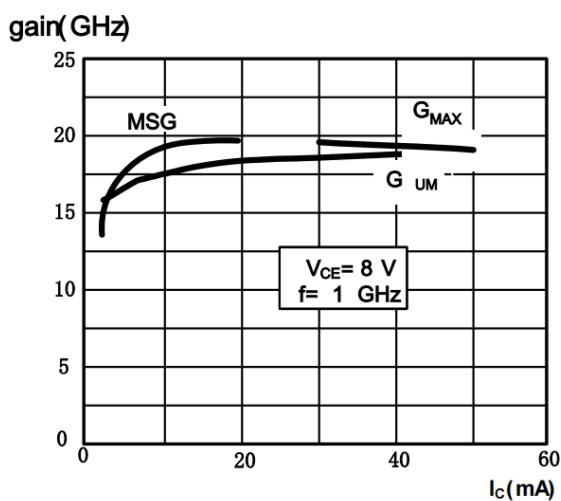
Frequency vs. Collector Current



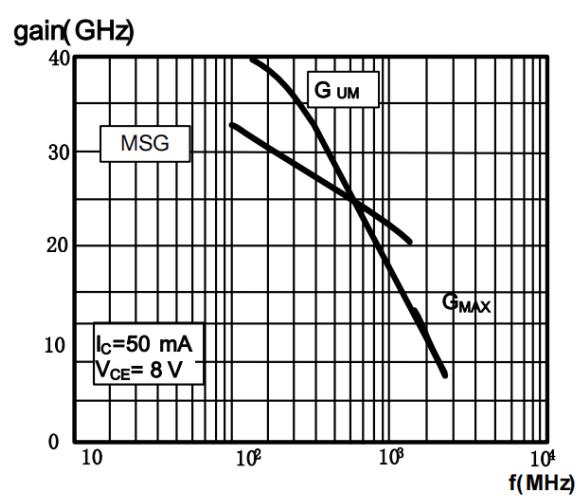
DC current vs. Collector Current

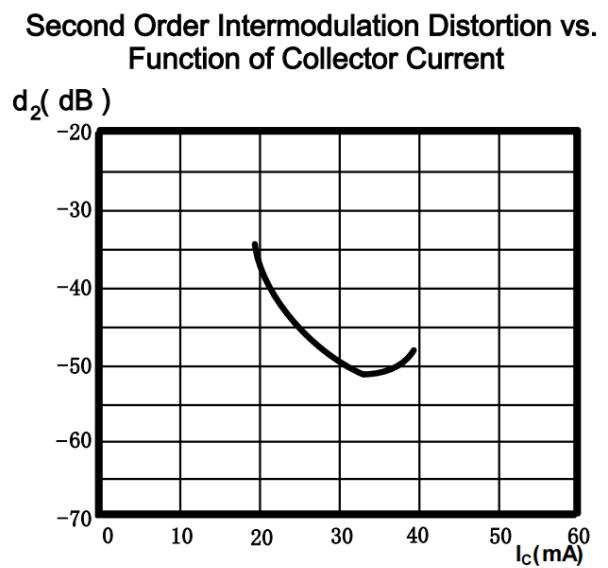
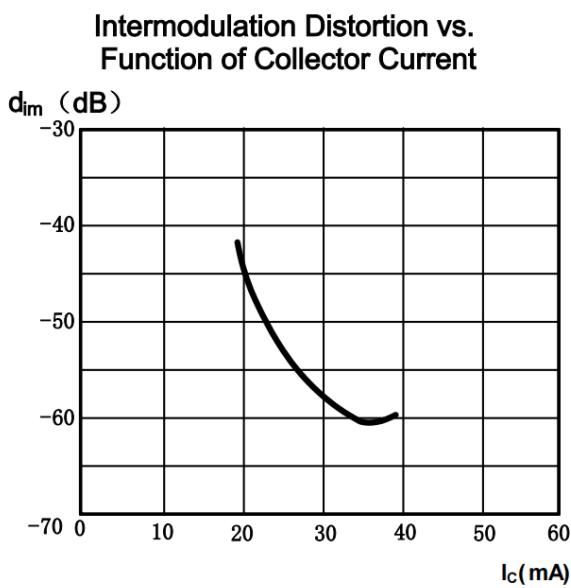
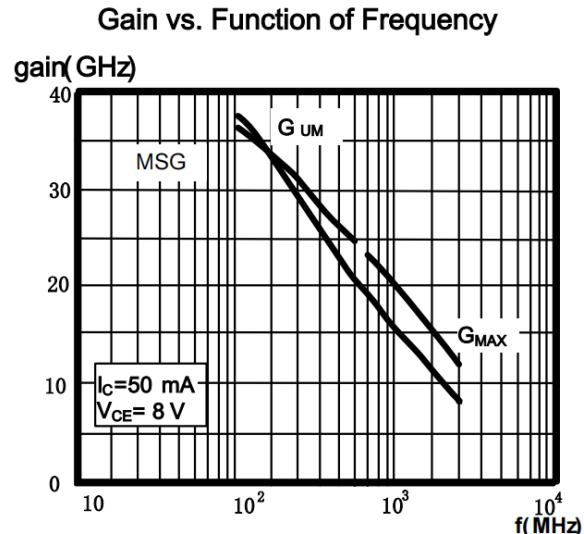
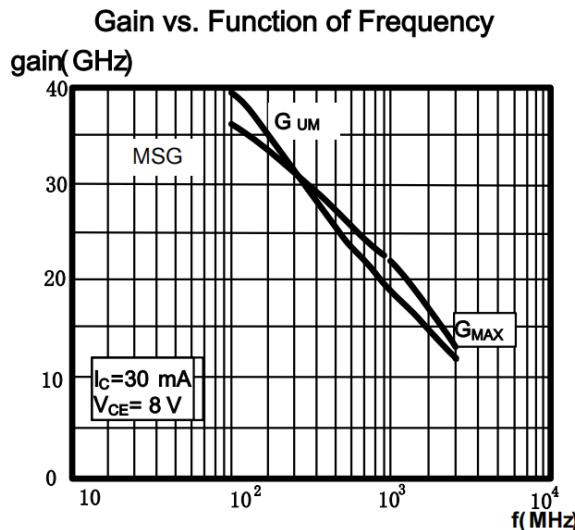


Gain vs. Function of Collector Current



Gain vs. Function of Frequency





Package Dimensions